

DEC 27 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/728,977
Filing Date December 8, 2003
Inventor Belford T. Coursey
Assignee Micron Technology, Inc.
Group Art Unit 2811
Examiner Unknown
Attorney Docket No. MI22-2459
Customer No. 021567
Title Memory Circuitry and Method of Forming Memory Circuitry

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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Dated: 12/27/2004By: Pat Palmer

Pat Palmer

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/728,977
	Filing Date	December 8, 2003
	First Named Inventor	Belford T. Coursey
	Art Unit	2811
	Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number	MI22-2459

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Firm Name	Wells St. John P.S.		
Signature			
Printed name	Mark S. Matkin		
Date	12/27/04	Reg. No.	32,268

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copy of the cited U.S. patent is included herewith. No admission is made regarding whether the listed reference is prior art.

Respectfully submitted,

Dated: 12-27-04By: 

Mark S. Matkin
Reg. No. 32,268

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. M122-2459		SERIAL NO. 10728,977	
LIST OF ART CITED BY APPLICANT <small>(Use several sheets if necessary)</small>					APPLICANT: Belford T. Coursey			
					FILING DATE December 8, 2003		GROUP 2811	
U.S. PATENT DOCUMENTS								
*Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate	
	AA	6,376,304 B1	04/23/02	Matsuoka et al.				
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AJ							
	AK							
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
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